



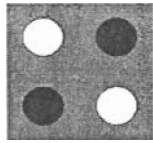
(B)

Worksheet 7 EEL 4705

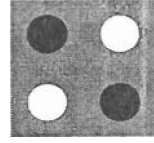
Emerging Logic Devices – QCA Logic Transfer

Group #: 14

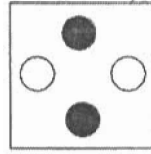
UIDs: [REDACTED]



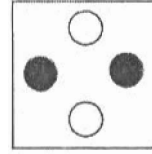
P = 1



P = 0



P = 1

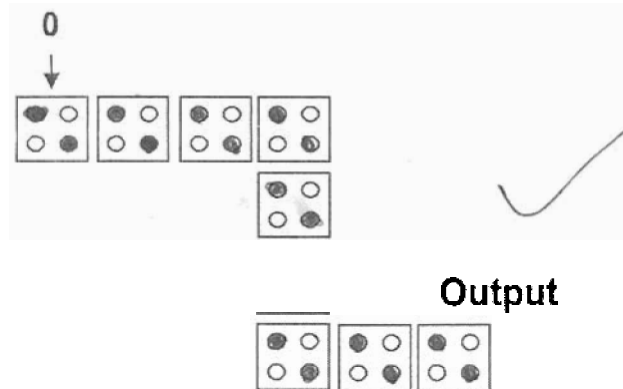


P = 0

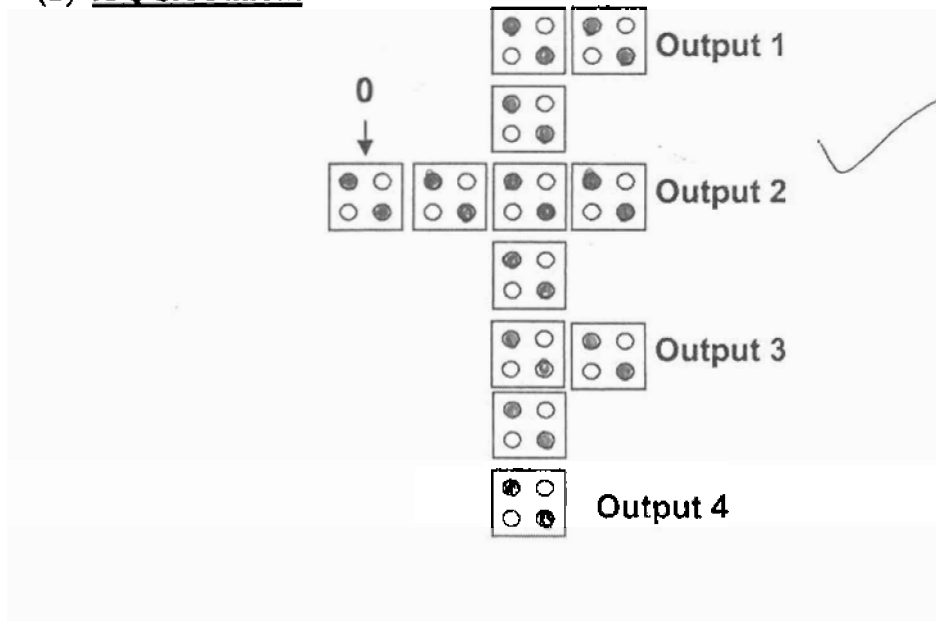
Question: Make use of the QCA Cell representation shown above to demonstrate how logic will propagate in the following QCA logic designs.

You need to fill out the dots in each QCA cell to represent whether it'll be in '1' or '0' state. Start from the Input Cell and fill out all the cells till the Output Cell in the same manner.

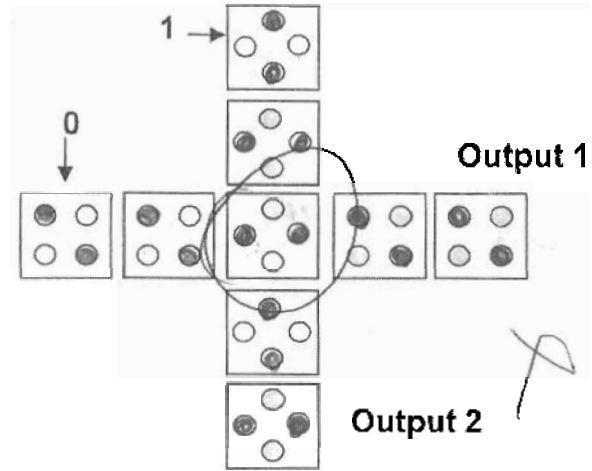
(a) A QCA Line



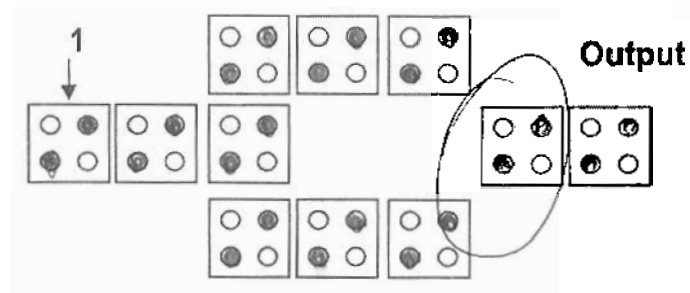
(b) A QCA Fanout



(c) A Crossbar



(d) A QCA Inverter



(e) A QCA Majority Gate

